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SHEET 3 OF 4

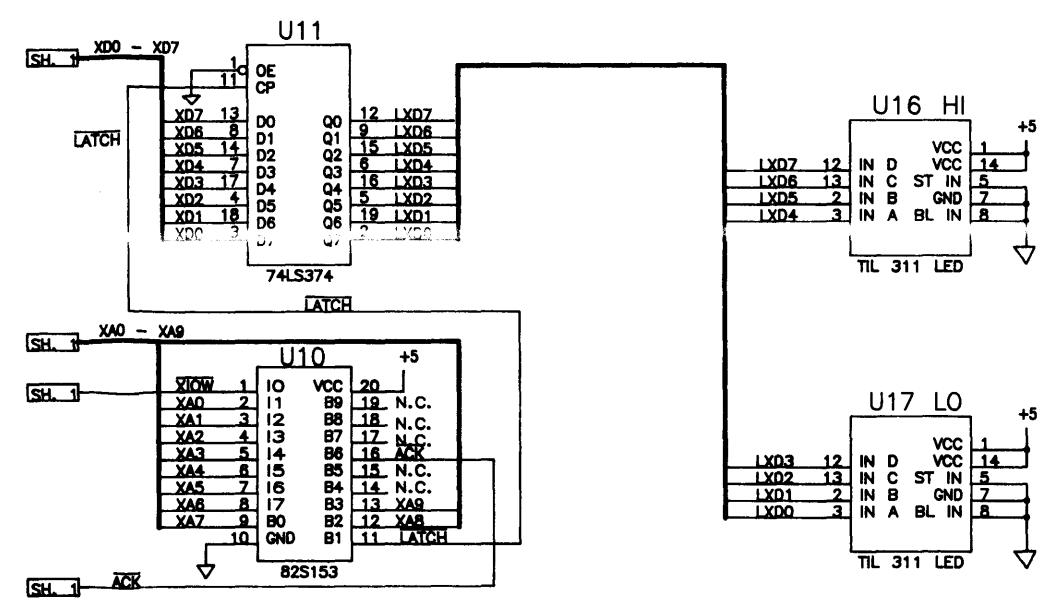
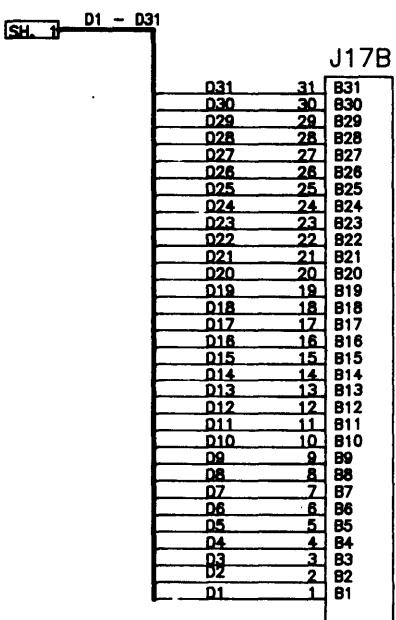
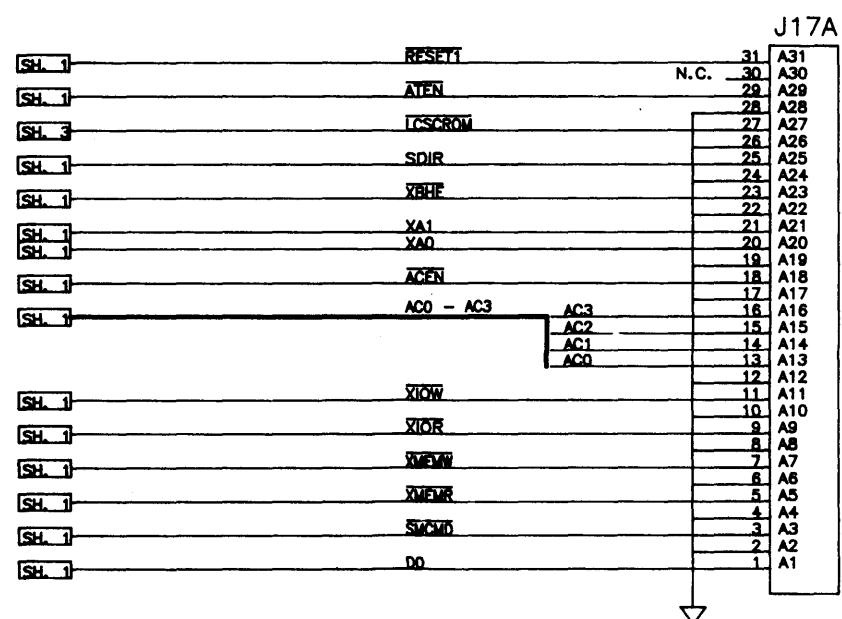
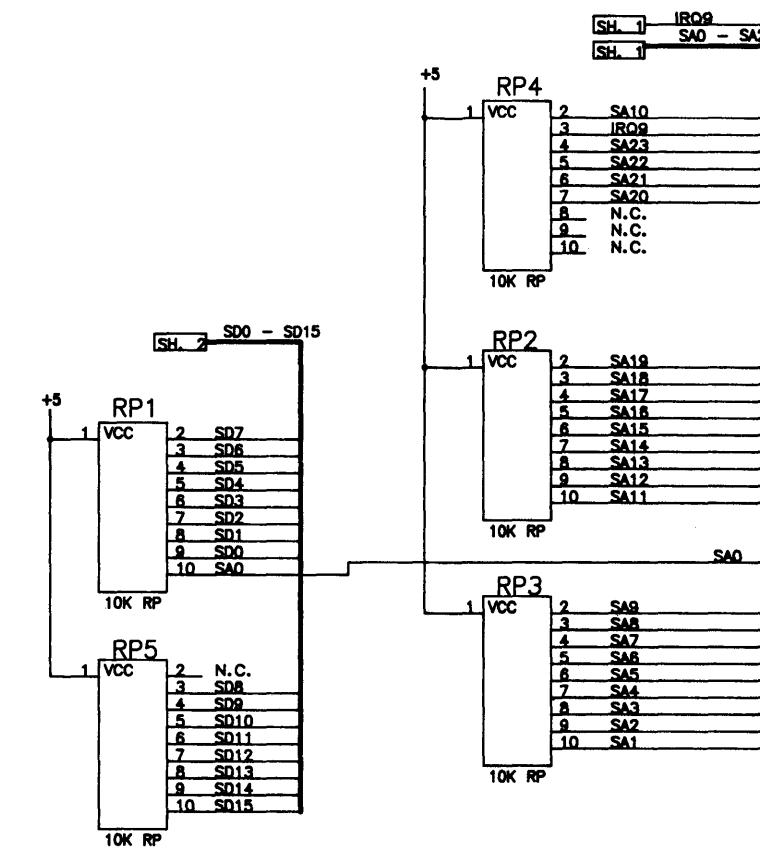
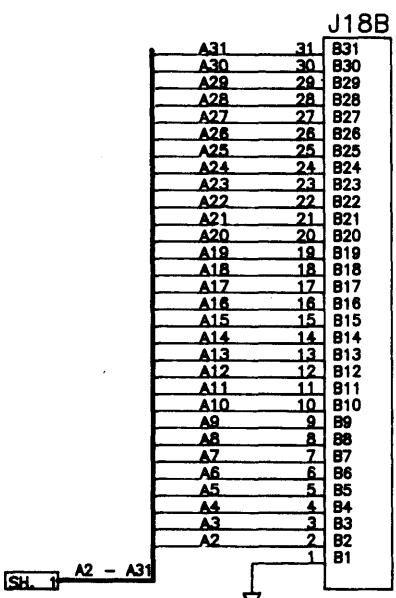
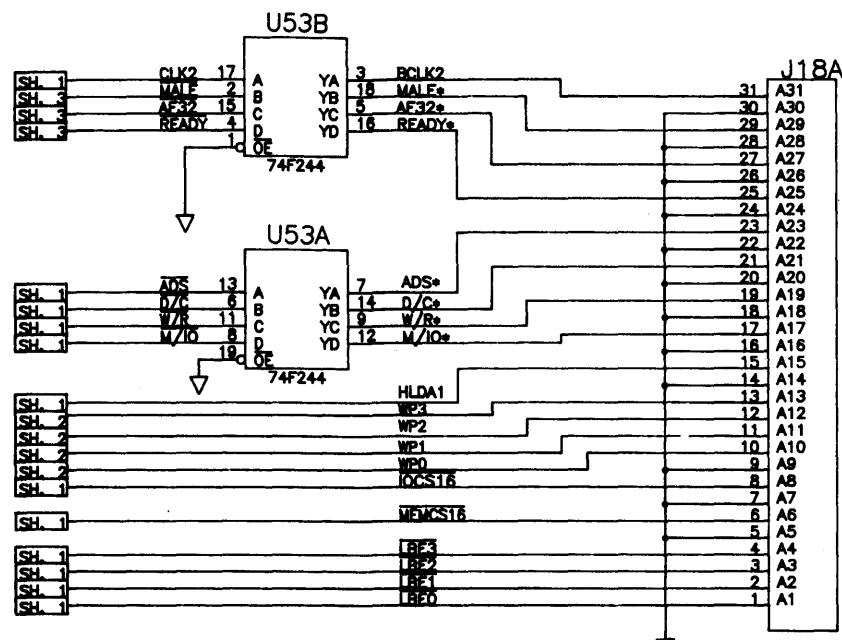
DK8230SCC

CHIPS AND TECHNOLOGIES, INC.

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B	3-25-87	LDK	INITAL RELEASE
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SHEET 4 OF 4			
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